



DATASHEET

ES-P-N2N1-2409



IEEE 820.11 b/g/n

High-Performance, Dual independent Radio, conforming to IEEE 802.11 b/g/n standards



Wide Frequency Support

Operating Frequency 2.3 GHz to 2.7 GHz and 902 MHz to 928 MHz



Industrial grade

-40 deg C to +85 deg C operation temperature





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TECHNICAL SPECIFICATION

	RADIO MODULE – GENERAL INFO
On-board Radio (11a/n) chipset MiniPCie Radio (11b/g/n) chipset	QCA 9550-AT4B (CPU) & AR 8033-AL1B (Ethernet PHY) AR 9592-AR1B
NOR Flash NAND Flash RAM	SPI Flash, 16MB NAND Flash, 256 MB DDR2, 200 MHz, 256 MB (64Mx16x2)
Operating frequency — on-board Radio Operating frequency — MiniPCle Radio	2300 MHz to 2700 MHz (Operating frequency range) 902 MHz to 928 MHz (Operating frequency range)
Data rate-11n HT20/HT40-1S (SISO) Data rate-11n HT20/HT40-2S (MIMO)	6Mbps, 9Mbps, 12Mbps, 24Mbps, 36Mbps, 48Mbps,54Mbps (11g) MCS0, MCS1, MCS2, MCS3, MCS4, MCS5, MCS6, MCS7 (11n) MCS8, MCS9, MCS10, MCS11, MCS12, MCS13, MCS14, MCS15 (11n)
Channel BW - On-board Radio (11b/g/n) Channel BW - MiniPCie Radio (11b/g/n)	5 MHz/10 MHz/20 MHz /40 MHz 5 MHz/10 MHz/20 MHz
RoHS Compliance	Compliant
	INTERFACE SPECIFICATIONS
Power in	Power Over Ethernet
Operating Voltage	9V to 30V
RF Antenna connector	x3 MMCX Male connectors
	ENVIRONMENTAL SPECIFICATIONS
Operating Temperature Range	-40 deg C to +85 deg C
	PHYSICAL SPECIFICATIONS
Mechanical Dimension	(L) 111.5 mm x (W) 84.6 mm
Weight	TBD PERCHAPITAL AND
Compliance	REGULATORY INFORMATION
Compliance	PACKAGING INFORMATION
No of units	TBD

ORDERING INFORMATION

ES-P-N2N1-2409

OEM PCB Assembly Kit, ES-P-N2N1-2409, HW Rev 02.00 SBC, 2.4 GHz, MIMO, HW Rev 2103.0800 MiniPCle, 900 MHz, SISO, HW Rev 2005.0300

RADIO SPECIFICATION

TX/RX Specification – 2412 MHz to 2462 MHz (on-board radio)

Sensitivity tested in ART Mode, PSR >=95%, TX Power Setting = Calibrated Power level in dBm Current consumption is measured at the input of the SBC, and it includes idle current drawn by the system

Data Rate	TX Power per chain (dBm)	Current 24V (A)	RX Sensitivity (dBm)
54 Mbps	20	0.230	-80
48 Mbps	21	0.245	-81
36 Mbps	23	0.265	-88
24 Mbps	25	0.290	-89
18 Mbps	25	0.290	-92
12Mbps	25	0.290	-94
9 Mbps	25	0.290	-95
6 Mbps	26	0.300	-96
11 Mbps	26	0.300	-91
5.5 Mbps	26	0.300	-96
2 Mbps	26	0.300	-98
1 Mbps	26	0.300	-99
HT20-MCS7	19	0.220	-74
HT20-MCS6	20	0.230	-75
HT20-MCS5	21	0.245	-76
HT20-MCS4	22	0.255	-81
HT20-MCS3	24	0.270	-85
HT20-MCS2	24	0.270	-89
HT20-MCS1	24	0.270	-91
HT20-MCS0	26	0.300	-93
HT40-MCS7	19	0.220	-71
HT40-MCS6	20	0.230	-72
HT40-MCS5	21	0.245	-73
HT40-MCS4	22	0.255	-78
HT40-MCS3	24	0.270	-82
HT40-MCS2	24	0.270	-86
HT40-MCS1	24	0.270	-88
HT40-MCS0	26	0.300	-90

TX/RX Specification – 902 MHz to 928 MHz

Sensitivity tested in ART Mode, PSR >=95%

TX Power and Sensitivity Tolerance = +/- 2 dBm

Current consumption is measured at the input of the SBC with the mini-PCle radio module connected to it. The current consumption figures are then adjusted so that they only include extra current drawn by the mini-PCle radio module.

Data Rate	TX Power per chain (dBm)	DC Power at 24V (W)	RX Sensitivity (dBm)
54 Mbps	20	2.2	-74
48 Mbps	22	2.4	-77
36 Mbps	24	2.6	-79
24 Mbps	26	2.9	-82
18 Mbps	26	2.9	-84
12Mbps	26	2.9	-86
9 Mbps	26	2.9	-89
6 Mbps	26	2.9	-91
11 Mbps	29	4.3	-85
5.5 Mbps	29	4.3	-90
2 Mbps	29	4.3	-92
1 Mbps	29	4.3	-93
HT20-MCS7	20	2.2	-65
HT20-MCS6	21	2.3	-67
HT20-MCS5	21	2.3	-71
HT20-MCS4	24	2.6	-75
HT20-MCS3	26	2.9	-78
HT20-MCS2	26	2.9	-81
HT20-MCS1	26	2.9	-86
HT20-MCS0	29	4.3	-88

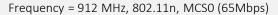
Channel Mapping - 902 MHz to 928 MHz

BASE BAND (MHz)	OP FREQ (MHz)	CH BW (MHz)	STANDARD (11b/g/n)
2427	907	5/10	11g/n
2432	912	5/10/20	11b/g/n
2437	917	5/10/20	11b/g/n
2442	922	5/10	11g/n

EVM PERFORMANCE

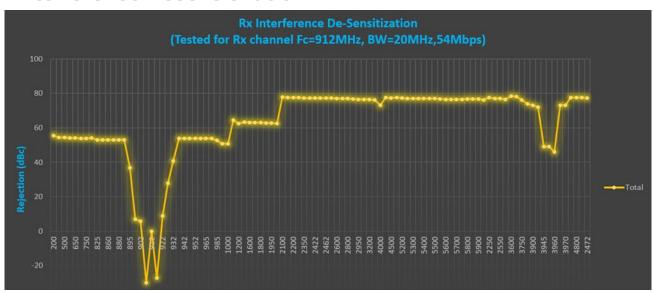
Frequency = 912 MHz, 802.11g, 54 Mbps

	t Rate							
64QAM 54.0	Mbps							
	Average	Max	Limit					
RMS EVM	-27.85 dB	-14.85 dB	-25.00 dB					
	4.05 %	18.09 %						
Peak EVM	-17.02 dB	-4.48 dB at sym 8						
	14.09 %	59.67 %						
Pilot EVM	-28.80 dB	-15.69 dB						
	3.63 %	16.42 %						
Data EVM	-27.78 dB	-14.79 dB						
	4.08 %	18.23 %						
Frequency Error	-2.66 ppm	-2.67 ppm	20.00 ppm					
Symbol Clock Error	-2.09 ppm	-9.60 ppm	20.00 ppm					
/Q Origin Offset	-38.96 dB	-23.57 dB	-15.00 dB					
Quadrature Skew	0.09°	-0.84 °		201				
IQ Gain Imbalance	-0.05 dB	-0.14 dB						
IQ Time Skew	-999.0 s	-999.0 s		35.				
Peak Burst Power	9.98 dBm	12.13 dBm		P-1				
Avg Burst Power	1.39 dBm	8.06 dBm						
Peak-to-Avg Power Ratio	8.6 dB	9.4 dB		-				
Time Offset	0.14 us	0.14 us						
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				42				



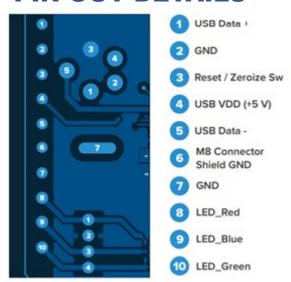
	Average	Max	Limit					
RMS EVM	-28.50 dB	-27.53 dB	-27.00 dB					
=	3.76 %	4.20 %						
Peak EVM	-17.67 dB	-15.36 dB at sym 36						
	13.08 %	17.06 %						
Pilot EVM	-29.55 dB	-28.16 dB						
	3.33 %	3.91 %						
Oata EVM	-28.43 dB	-27.47 dB						
	3.79 %	4.23 %						
requency Error	-2.83 ppm	-2.85 ppm	25.00 ppm					
Symbol Clock Error	-3.11 ppm	-5.86 ppm	25.00 ppm					
Q Origin Offset	-48.50 dB	-44.45 dB	-15.00 dB					
Quadrature Skew	0.47°	0.63°						
Q Gain Imbalance	0.00 dB	-0.03 dB						
Q Time Skew	43.67 ps	87.44 ps						
Peak Burst Power	9.90 dBm	10.85 dBm						
lvg Burst Power	1.10 dBm	1.19 dBm						
Peak-to-Avg Power Ratio	8.8 dB	9.8 dB						
ime Offset	0.15 us	0.15 us						

Interference Desensitization



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PIN OUT DETAILS



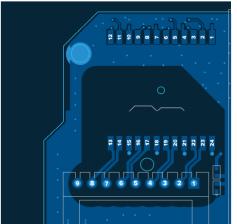
The mating connectors for DF3-10P-2DS(01) are

PCB Mounted: DF3-10S-2DSA(25)

PCB Mounted: DF3-9S-2DSA(25)

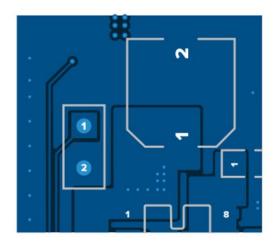
Cable Housing: DF3-9S-2C

Cable Housing: DF3-10S-2C



- 1 DA+
 2 DAThe mating connectors for DF3-9P-2DS(01) are
- 3 DB+
- 4 DB -
- 5 DC +
- 7 DD+
- 8 DD -
- SHIELD (connected through
 1 nF Capacitor to System GND)

A DC power interface is provided through a 2-pin Hirose DF3-2P-2DS(01) connector with the following pinout:

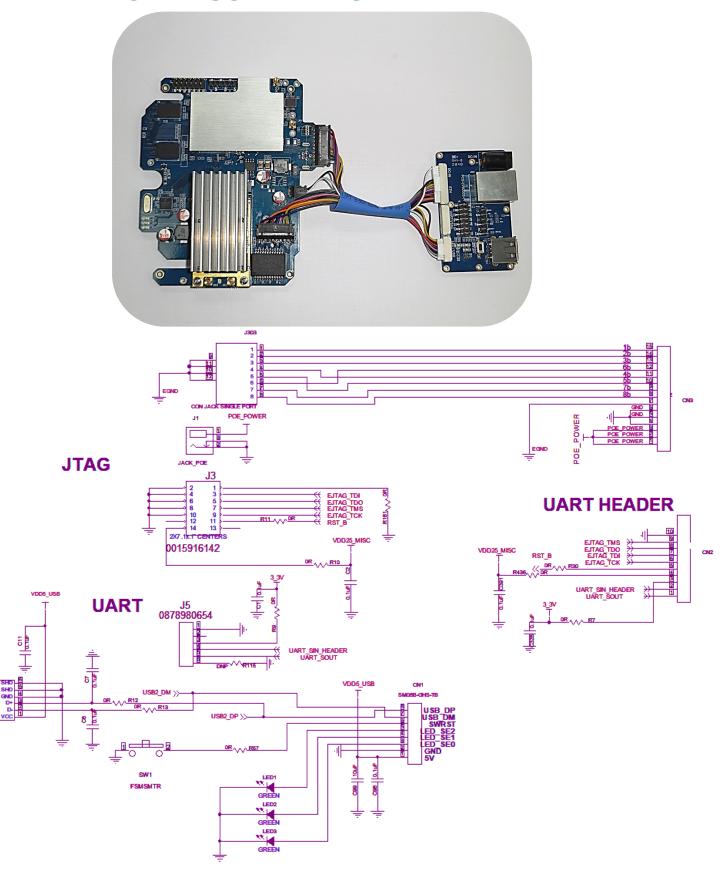




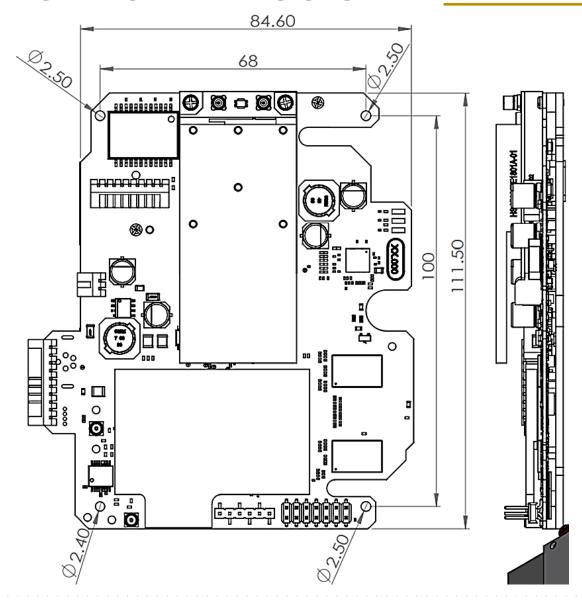
The mating connectors for DF3-2P-2DS(01) are

- PCB Mounted: DF3-2S-2DSA(25)
- · Cable Housing: DF3-2S-2C

EVAL BOARD SCHEMATIC



MECHANICAL DIMENSIONS





Contact

Web: https://vizmonet.com Email: enquiry@vizmonet.com Headquarters

Vizmonet Pte Ltd 21, Woodlands Close #03-01, Primz Biz Hub Singapore 737 854

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